



WarpAttack: Bypassing CFI through Compiler-Introduced Double-Fetches

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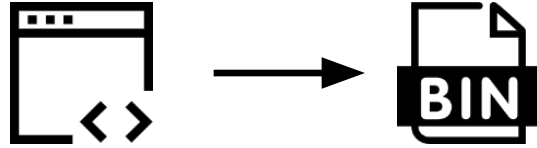
Compiler-Introduced Double Fetch

A Linux kernel case (2012):

```
1  /* commit: 8135cf8b092723dbfcc611fe6fdcb3a36c9951c5 */
2  switch (op->cmd) {
3      case XEN_PCT_OP_conf_read:
4          op-12 /* the corresponding assembly code */
5              13  cmp  DWORD PTR [r13+0x4], 0x5
6              bre  14  mov  DWORD PTR [rbp-0x4c], eax
7      case XEN_PCT_OP_conf_write:
8          //
9      default:
10         op-17  jmp  QWORD PTR [rax*8+off_77D0]
11     }
```

Compiler & Correctness-Security Gap

- Compiler

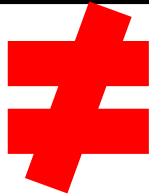


- Correctness-Security Gap

```
// Attempt to scrub the sensitive data saved on  
stack  
memset(secret, 0, sizeof(secret));  
return;
```

Correctness

Dead Store



Security

Sensitive Data Scrubbing

Compiler-Introduced Security Issues

- WarpAttack key insight

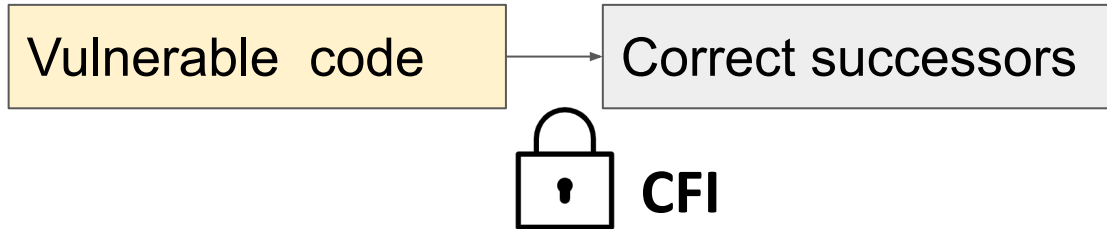
| | Compiler Correctness | Security |
|------------------------------------|--|-----------------------------------|
| Compiler-Introduced Double-Fetches | Concurrency bugs Or Benign data race | A weakness of control flow guards |

WarpAttack exploits a misalignment between compiler implementations and CFI assumptions

Control Flow Integrity (CFI)

CFI guard control flow

- Inserts run-time checks
- Practical and (reasonably) fine-grained



CFI is getting more and more important

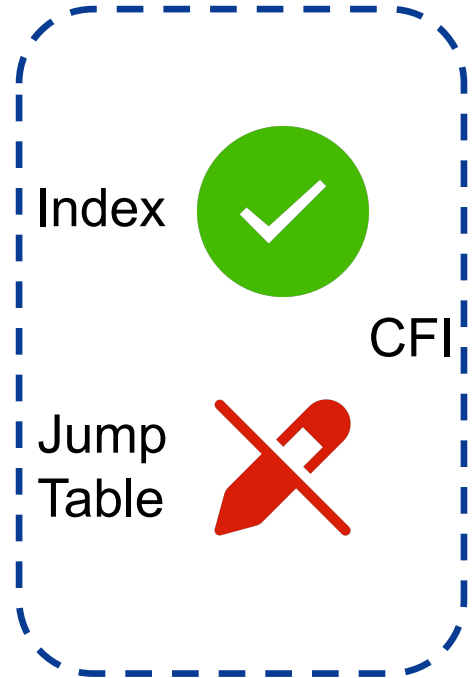
Bound-Checked Indirect Jumps

CFI: no need to protect

```
switch(a)
  case 0:
  case 1:
  ...
  default:
  ...
}
```

Assembly code snippets:

- `mov r1, [A]`
- `cmp r1, imm`
- `mov r1, [r3+r1*4]`
- `add r1, r3`
- `jmp r1`



~~Bound-Checked~~ Indirect Jumps

Compilers are not aware of security boundaries

```
mov r1, [A]  
cmp r1, imm  
...  
mov r1, [r3+r1*4]  
add r1, r3  
jmp r1
```

```
mov r1, [A]  
cmp r1, imm  
...  
mov r2, [A]  
mov r2, [r3+r2*4]  
add r2, r3  
jmp r2
```

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CFI

Jump



WarpAttack: Threat model

Adversarial Capabilities

- Arbitrary read-write
- Thread control
- **One triggerable gadget sample**

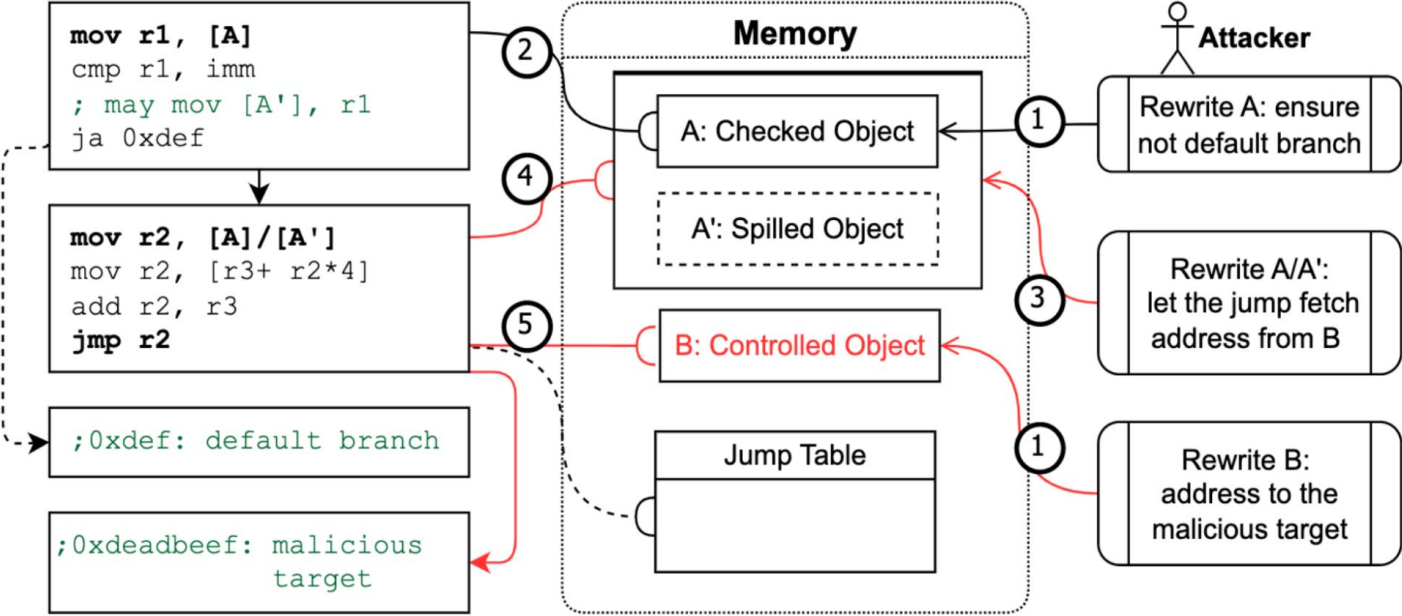
Defensive Assumptions

- Non-Executable Memory
- Randomization
- Control Flow Protection



The only requirement beyond CFI's Threat model.

WarpAttack



Challenges and Solutions

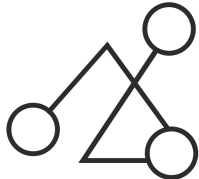
Gadget Code Detection:

Challenges

Compiler-Introduced?



Data Dependency

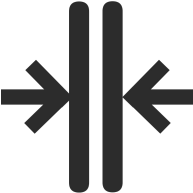
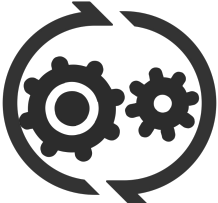




Solutions




Challenges and Solutions

Proof-of-Concept Exploit:

| Challenges | Solutions |
|--|--|
| Short time window  |  |
| Crashes when wrong  |  |

0.45% success rate in 20 seconds

On Firefox 106.0.1



How WarpAttack affects real world

Vulnerable code in the wild

- All C/C++ programs potentially affected
- 1,600+ victim gadgets in 6 programs

Acknowledgements from



Affected Compilers

- GCC and clang

Other than X86/64

- ARM 32/64*
- RISCV 32/64
- MIPS 32/64

Only X86/64 has two variants,

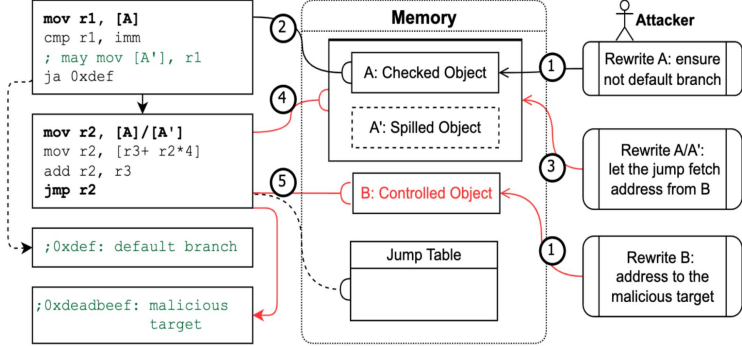
Others have just stack spilling variant

MOSEC2019
Brandon Azad

WarpAttack affects many programs, compilers and architectures

WarpAttack Conclusion

- CFI assumption
- Attack method
- Real world Impact
- Proof-of-Concept

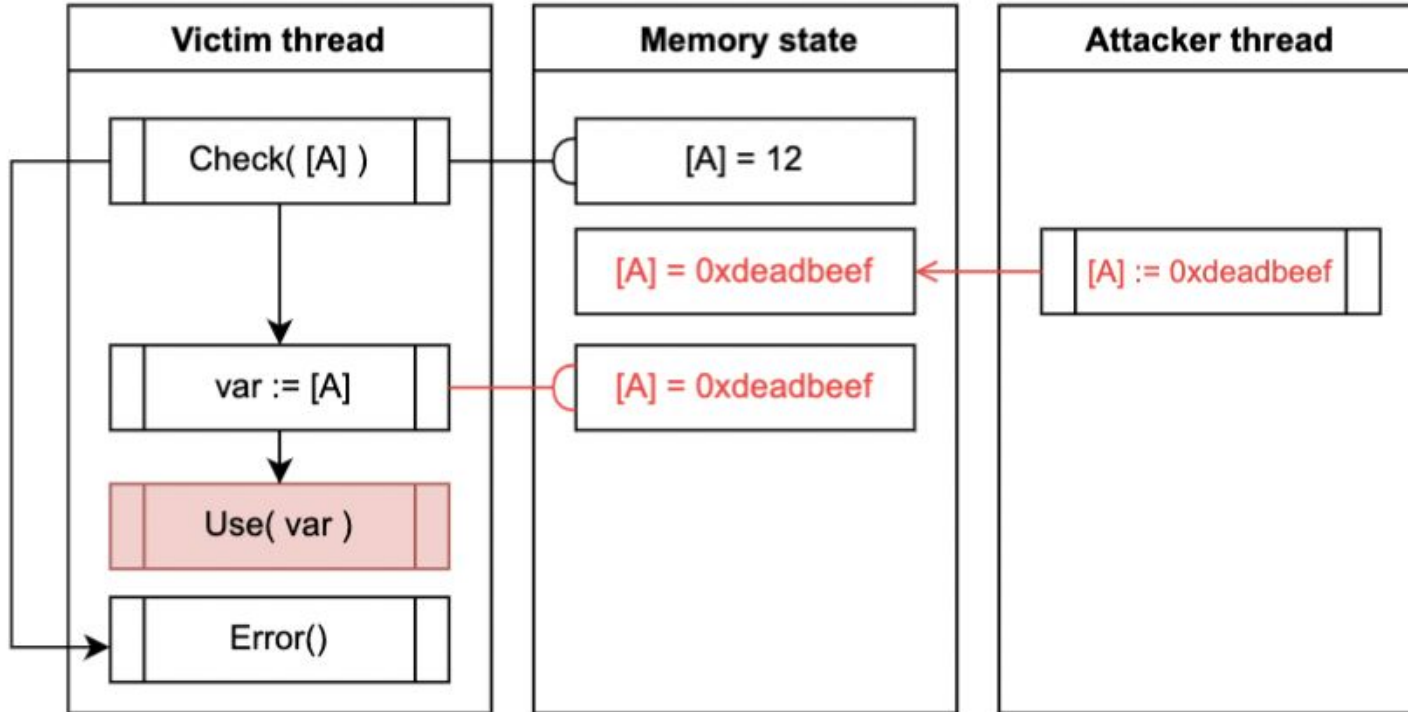


Thanks!



- Backup slides

Double Fetch



Mitigations

Avoiding Gadget code generation

- GCC '-fno-switch-tables'
- Clang 'O1'

Protecting Indirect Jump

- CFI checks for switch jump tables

Monitoring for Attack Behavior

- Characteristics like spawning several threads, constantly writing a certain memory site
- Crashes